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The *International Symposium on Computer Architecture* is the premier forum for new ideas and experimental results in computer architecture. Novel papers are solicited on a broad range of topics, including (but not limited to):

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- Effect of circuits and technology on architecture
- Architecture modeling and simulation methodology
- Performance evaluation and measurement of real systems
- Data-center scale computing
- Handheld and mobile devices



IMPORTANT DATES:

Abstract Deadline:	November 14th, 2011, 11:59PM CST
Final Paper Deadline:	November 21st, 2011, 11:59PM CST
Workshop/Tutorial Proposals:	December 16th, 2011
Rebuttal Period:	February 10th-13th, 2012
Author Notification:	March 5th, 2012

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